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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Terunao HANAOKA et al.

Application No.: 10/767,999

Docket No.: 109690.02

Filed: February 2, 2004

For: Semiconductor Device, Method of Fabricating the Same, Stack-Type Semiconductor Device, Circuit Board and Electronic Instrument

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

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Date: June 3, 2004

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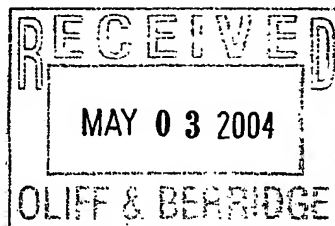
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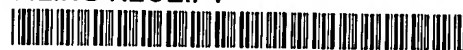
APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/767,999	02/02/2004	2811	856	109690.02	17	5	4

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CONFIRMATION NO. 5935

FILING RECEIPT



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Date Mailed: 04/30/2004

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Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/870,710 06/01/2001 PAT 6,720,661

Foreign Applications

JAPAN JP2000-166104 06/02/2000
 JAPAN JP2000-338737 11/07/2000
 JAPAN JP2001-125581 04/24/2001

If Required, Foreign Filing License Granted: 04/30/2004

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Early Publication Request: No

Title

Semiconductor device, method of fabricating the same, stack-type semiconductor device, circuit board and electronic instrument

Preliminary Class

257

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